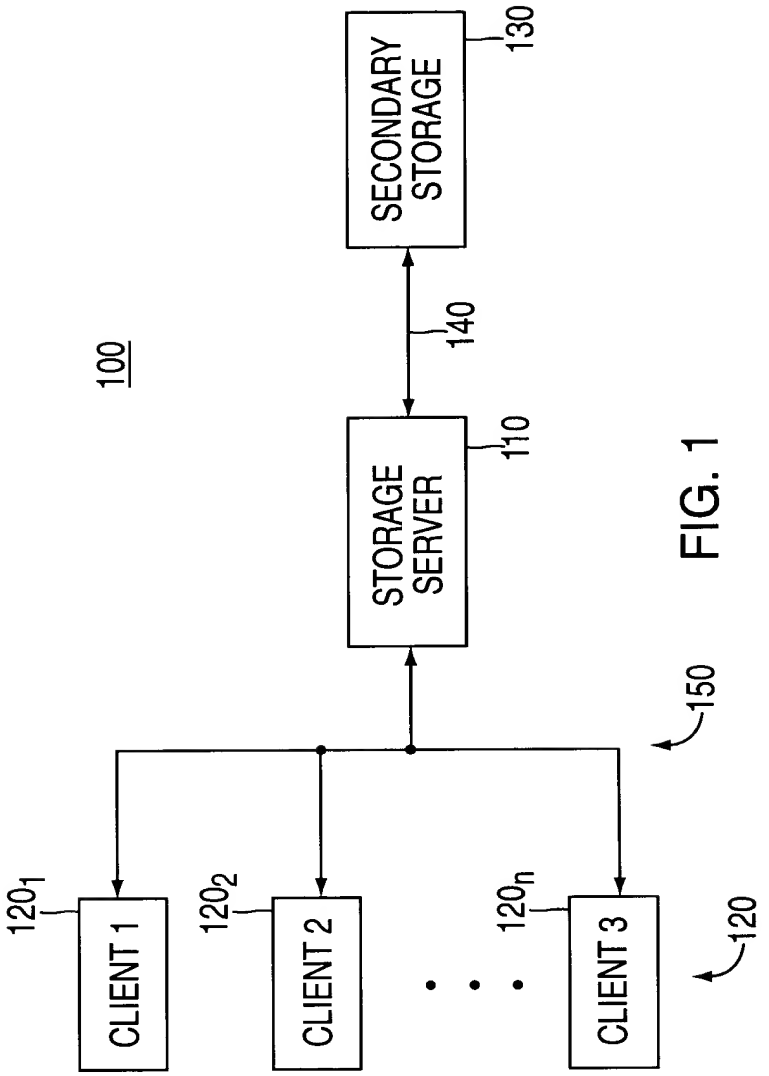
100
SECONDARY STORAGE
130
140
STORAGE SERVER
110
150
120_1
CLIENT 1
120_2
CLIENT 2
120_n
CLIENT 3
120

FIG. 1

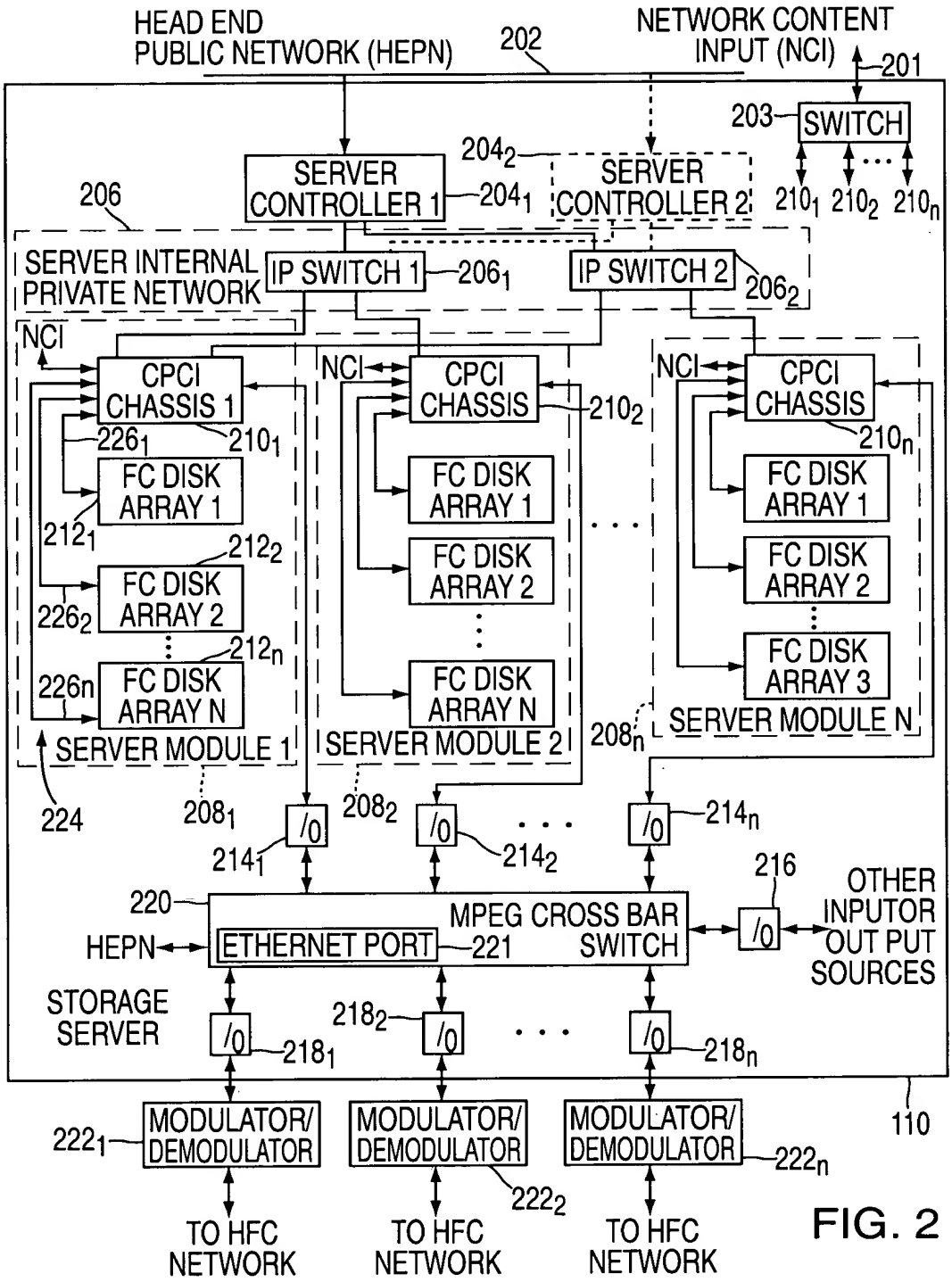
HEAD END PUBLIC NETWORK (HEPN)
202
NETWORK CONTENT INPUT (NCI)
201
203
SWITCH
210_1 210_2 210_n
204_2
SERVER CONTROLLER 1
204_1
SERVER CONTROLLER 2
206
SERVER INTERNAL PRIVATE NETWORK
IP SWITCH 1
206_1
IP SWITCH 2
206_2
NCI
CPCI CHASSIS 1
210_1
226_1
FC DISK ARRAY 1
212_1
212_2
FC DISK ARRAY 2
226_2
212_n
226n
FC DISK ARRAY N
SERVER MODULE 1
224
208_1
CPCI CHASSIS
210_2
FC DISK ARRAY 1
FC DISK ARRAY 2
FC DISK ARRAY N
SERVER MODULE 2
208_2
CPCI CHASSIS
210_n
FC DISK ARRAY 1
FC DISK ARRAY 2
FC DISK ARRAY 3
SERVER MODULE N
208_n
I/O
214_1
214_2
214_n
216
OTHER INPUTOR OUT PUT SOURCES
220
MPEG CROSS BAR SWITCH
HEPN
ETHERNET PORT
221
STORAGE SERVER
218_1
218_2
218_n
MODULATOR/ DEMODULATOR
222_1
222_2
222_n
TO HFC NETWORK
110

FIG. 2

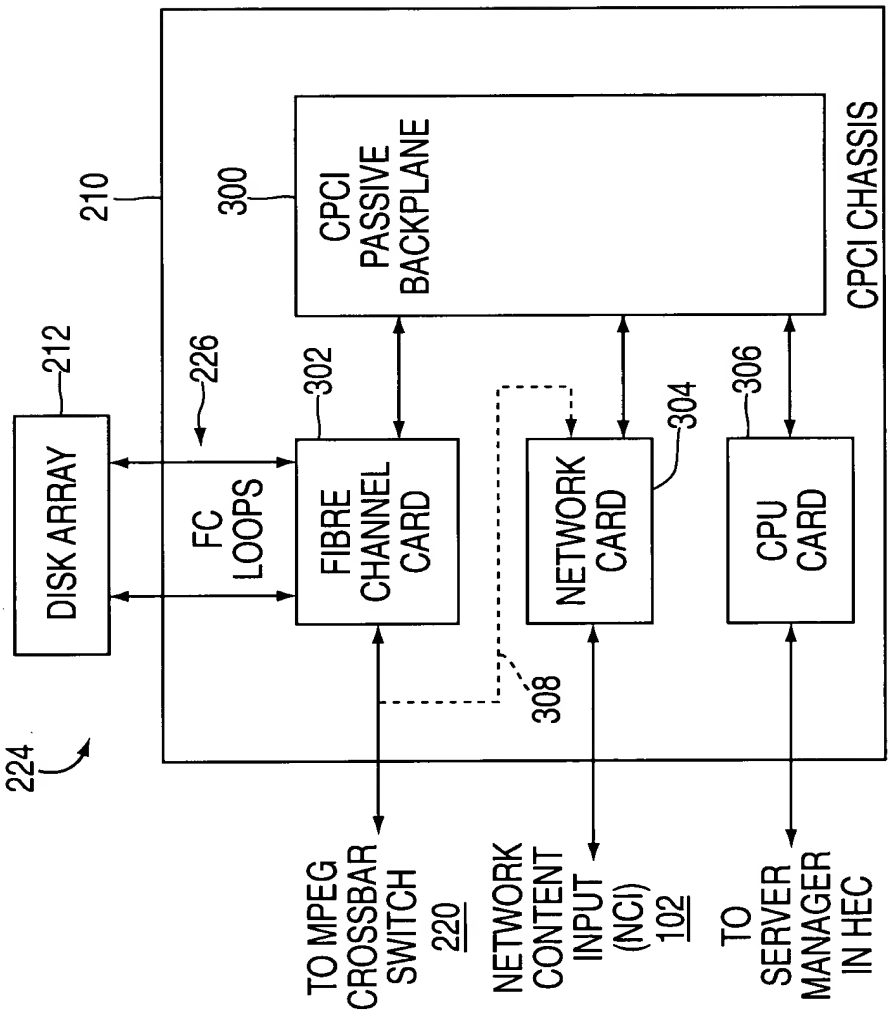
210
300
CPCI PASSIVE BACKPLANE
CPCI CHASSIS
212
DISK ARRAY
226
FC LOOPS
302
FIBRE CHANNEL CARD
304
NETWORK CARD
306
CPU CARD
308
224
TO MPEG CROSSBAR SWITCH 220
NETWORK CONTENT INPUT (NCI) 102
TO SERVER MANAGER IN HEC

FIG. 3

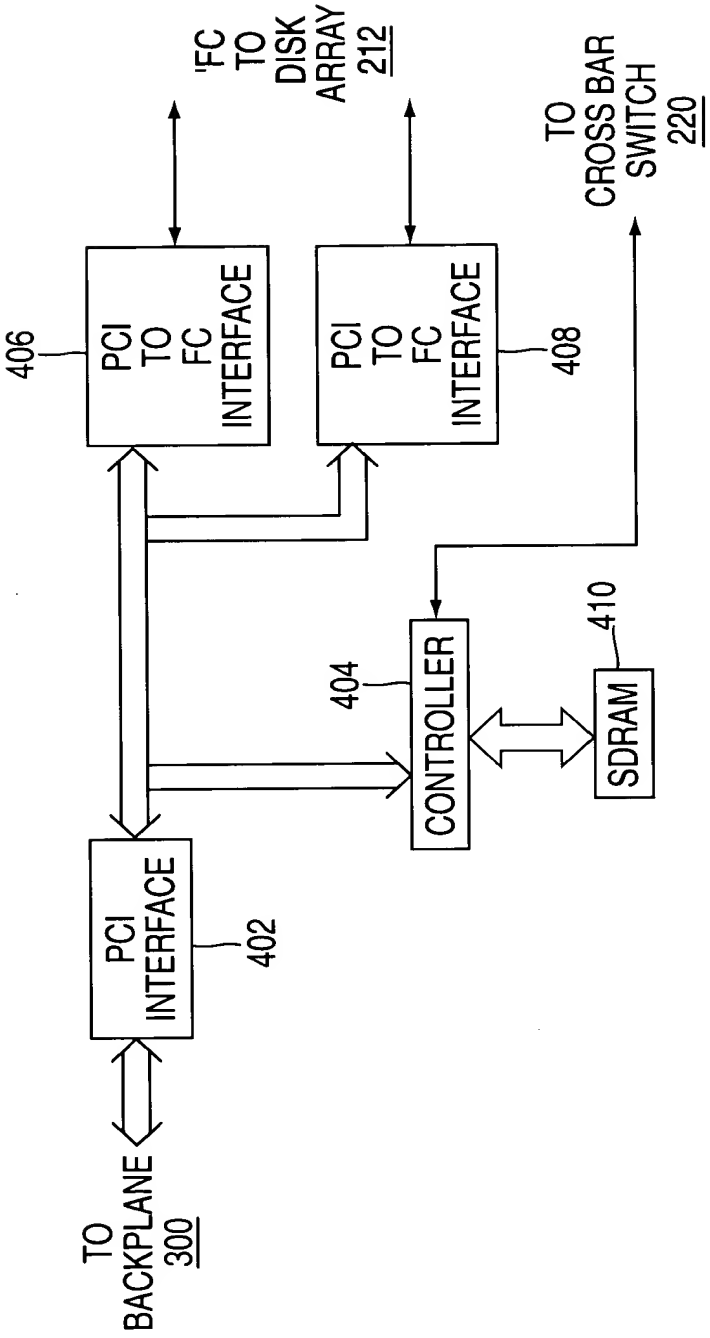
TO BACKPLANE 300
PCI INTERFACE
402
CONTROLLER
404
SDRAM
410
PCI TO FC INTERFACE
406
PCI TO FC INTERFACE
408
FC TO DISK ARRAY 212
TO CROSS BAR SWITCH 220

FIG. 4

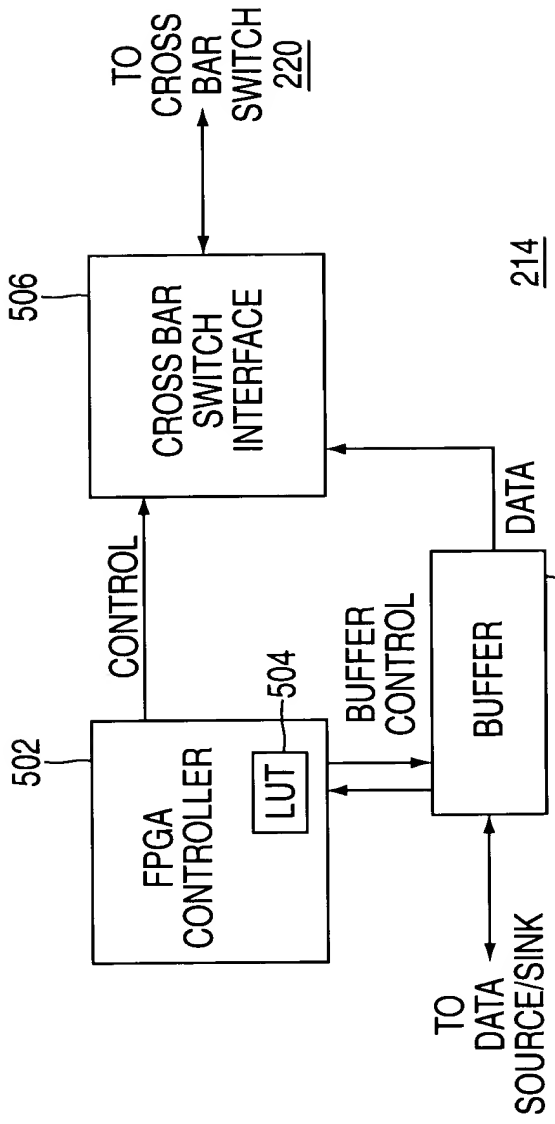
TO CROSS BAR SWITCH 220
506
CROSS BAR SWITCH INTERFACE
214
DATA
CONTROL
504
BUFFER CONTROL
BUFFER
508
502
FPGA CONTROLLER
LUT
TO DATA SOURCE/SINK

FIG. 5

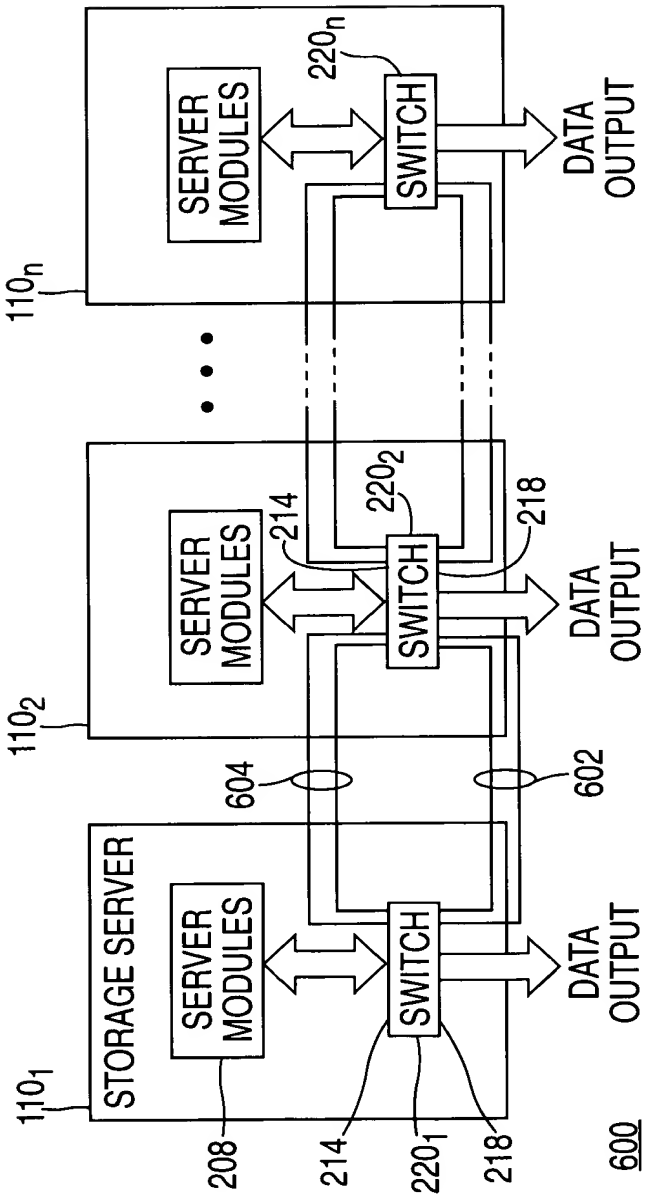
STORAGE SERVER
SERVER MODULES
SWITCH
DATA OUTPUT
110_1
110_2
110_n
220_1
220_2
220_n
208
214
218
602
604
600

FIG. 6